

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Kazunori Yamashita
Appl. No. : --
Filed : Concurrently herewith
For : A CIRCUIT BOARD CONNECTOR AND METHOD OF ASSEMBLING
IT

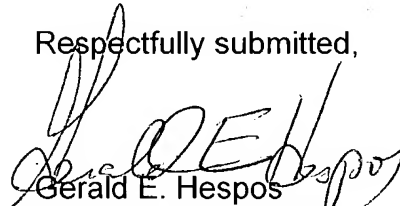
MS Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement includes a copy of Form PTO-1449 that identifies one prior Japanese Unexamined Utility Model Publication. A copy of the identified reference is attached as well. The relevancy of this document is described in paragraphs 0002-0004 of the subject application. The Examiner is requested to consider this reference during the examination and to make the reference of record.

Respectfully submitted,



Gerald E. Hespos
Atty. Reg. No. 30,066
Customer No. 001218
CASELLA & HESPOS LLP
274 Madison Avenue - Suite 1703
New York, NY 10016
Tel. (212) 725-2450
Fax (212) 725-2452

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INFORMATION DISCLOSURE CITATION <i>(Use several sheets if necessary)</i>	Docket Number (Optional) FP03-013US	Application Number --
	Applicant(s) Kazunori Yamashita	
	Filing Date Concurrently herewith	Group Art Unit --

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO
		61-60486	1986	Japan				✓

OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, Etc.)*

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.